

# Protectli Appliance

Protectli Vault Pro VP6650

2x 10G, 4x 2.5G Intel® i5-1235U

December 15<sup>th</sup>, 2025

# Overview

The Protectli Vault Pro VP6650, features the Intel® Core™ i5-1235U Processor with 4x Intel i226-V 2.5G Network ports and slots for up to 2x Intel X710 10G Network ports. The VP6650 supports up to 64GB DDR5 RAM and includes additional M.2 slots for optional NVMe SSD storage, WiFi, and LTE modules.

Protectli Vaults utilize Intel® components ensuring persistent compatibility with a wide range of operating systems (OS) and applications. The "VP66xx" series Vaults features the same Protectli all-aluminum chassis but with a twist: this series may have a fanless appearance, but they are designed with two additional PWM fans for improved heat dissipation from our highest performing vault series.

## Technical Specifications

<b>Model</b>	VP6650
<b>Description</b>	2x 10G, 4x 2.5G Network Port Appliance
<b>Processor</b>	Intel® Core™ i5-1235U (64 bit, Max 4.4 GHz)
<b>Processor Cores</b>	10
<b>Processor Threads</b>	12
<b>Intel® AES-NI</b>	Supported
<b>Virtualization</b>	Intel® Vt-x, Vt-d
<b>Network</b>	2x Intel® X710-BM2 SFP+, 4x Intel® I226-V Ethernet RJ-45
<b>Video / Graphics</b>	Intel® Iris Xe Graphics, 1x HDMI 1.4, 1x DP 1.4a
<b>Audio</b>	Audio over HDMI
<b>Memory</b>	2x SO-DIMM DDR5-4800, Max 64GB
<b>Storage</b>	1x M.2 2280 NVMe
<b>Optional Storage</b>	2x Internal 2.5" SATA 3.0 SSD
<b>External I/O</b>	2x 10G SFP+, 4x 2.5G Ethernet, RJ-45 1x USB 3.2 Gen 2 Type A, 3x USB 2.0 Type A 1x USB 3.2 Gen 2 Type C with DisplayPort 1x RJ-45 COM, 1x USB Type C COM Port 1x HDMI 1x DisplayPort 1x Nano (4FF) SIM Holder 6x WiFi/LTE Antenna Mounting Holes 1x 12V DC Power Jack, Threaded

<b>Internal I/O</b>	1x M.2 2280 M-Key PCIe 4.0 x4 (NVMe) 2x SATA Header, 2x SATA Power 1x M.2 2230 E-Key PCIe 3.0 x1 for WiFi 1x M.2 3052 (LTE) 1x USB 2.0 Header 1x Trusted Platform Module Header (2x6 pin) 1x CMOS Reset (2 pin) 2x PWM Fan Headers (4 pin, 12v) 1x Front Panel Header (9 pin)
<b>BIOS</b>	AMI®
<b>Indicators</b>	1x LED Power Button (Blue), 1x LED Power Indicator (Green), 1x LED Disk Activity Indicator (Red), 1x LED Disk Activity Indicator (Yellow)
<b>Power</b>	Input 12V DC, 1x DC Power Jack, Threaded connector
<b>Power Usage</b>	Idle: 12W, Max: 100W
<b>Chassis</b>	Aluminum, Gray
<b>Chassis Dimensions</b>	7.5 x 7 x 3 in, 191 x 178 x 76 mm
<b>Mounting Options</b>	Desktop, VESA Bracket, Optional 1RU Rack Mount
<b>Weight</b>	5 lbs, 2.3 Kg
<b>Shipping Weight</b>	5 lbs 13 oz, 2.6 Kg
<b>Operating Temperature</b>	+14° - +122° F, -10° - +50° C
<b>Operating Humidity</b>	0 – 95% relative humidity, non-condensing
<b>Approvals</b>	UL (Power Supply), FCC Part 15 Class B, CE, RoHS
<b>Country of Origin</b>	Made in China, Assembled in USA, Canada, or Germany
<b>Optional WiFi</b>	1x M.2 2230 E-Key PCIe 802.11ac/a/b/g/n (PCIe)
<b>Optional LTE Cellular</b>	1x M.2 3052 B-Key USB 3.2 Gen 1 (LTE), with Nano (4FF) SIM holder
<b>Optional TPM</b>	1x Trusted Platform Module, TPM 2.0

## Included Accessories and Components

120W Power Supply with barrel connector

US/CA Power Cable *(Other regional power cables available)*

USB Type-C (with Type-A adapter) to USB Type-C Serial Console Cable

8x SSD mounting screws

2x SATA power cables

2x SATA data cables

Heat sink with thermal pad and mounting hardware

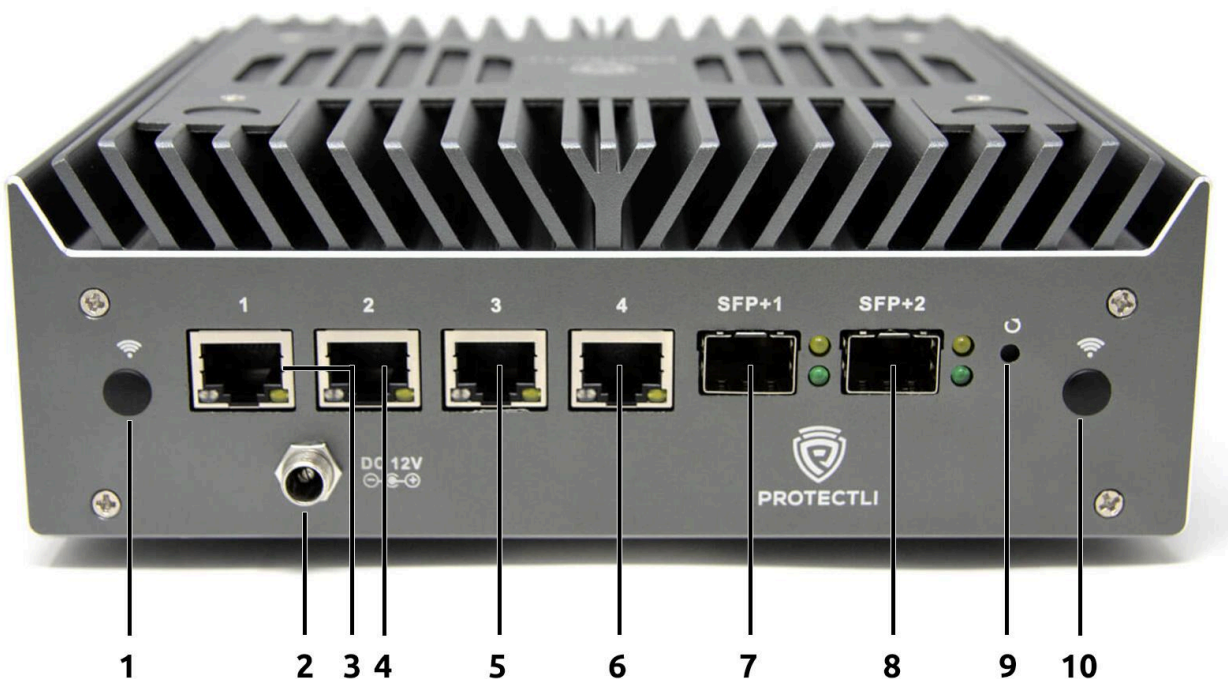
4x M2 screws


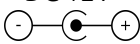
VESA Bracket mount with hardware


Quick Start Guide

# External Interfaces

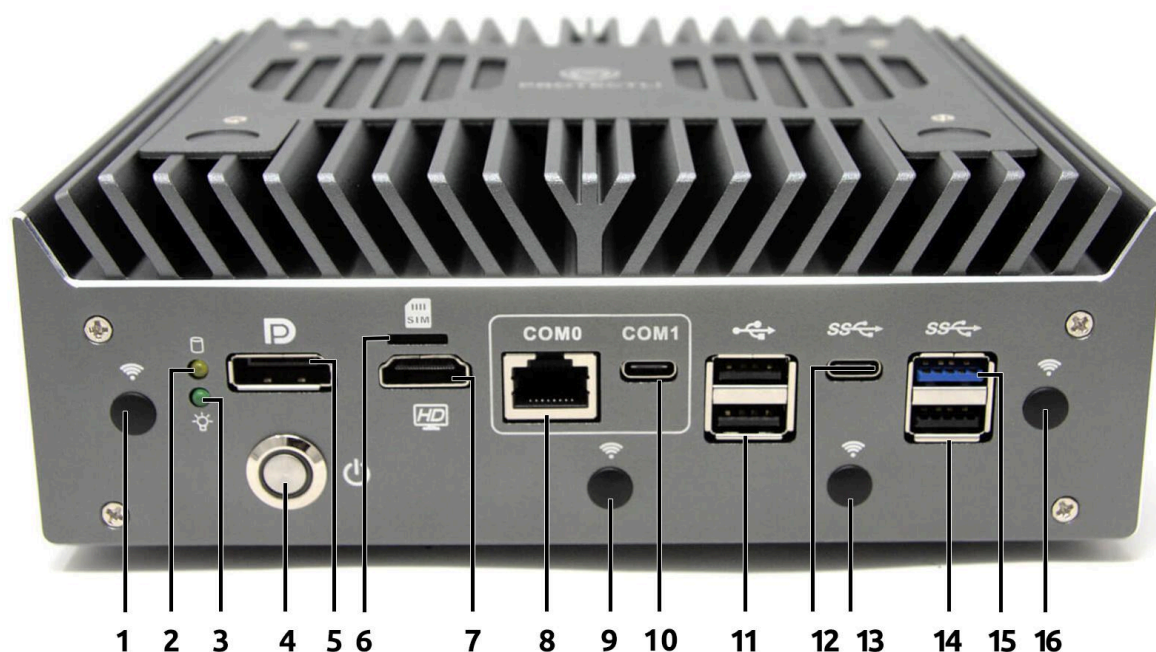
## Front Panel Configuration













Item #	Object	Label	Description
1, 10	Antenna Ports		Two antenna ports for adding radio antennas (WiFi, LTE, etc.). The ports are covered by plugs while not in use.
2	Power Supply Connector	DC 12V 	12V DC threaded barrel connector for the 120W external power supply. Positive rail is the tip, negative is sleeve.
3	Ethernet Port 1	1	The first 10/100/1000/2500 Mbps Intel® i226-V ethernet port.  [Left LED will illuminate Amber at 2500Mbps, Green at 1000Mbps, and will be turned off when connected at 100/10Mbps]
4	Ethernet Port 2	2	The second 10/100/1000/2500 Mbps Intel® i226-V

			ethernet port.  [Left LED will illuminate Amber at 2500Mbps, Green at 1000Mbps, and will be turned off when connected at 100/10Mbps]
5	Ethernet Port 3	3	The third 10/100/1000/2500 Mbps Intel® i226-V ethernet port.  [Left LED will illuminate Amber at 2500Mbps, Green at 1000Mbps, and will be turned off when connected at 100/10Mbps]
6	Ethernet Port 4	4	The fourth 10/100/1000/2500 Mbps Intel® i226-V ethernet port.  [Left LED will illuminate Amber at 2500Mbps, Green at 1000Mbps, and will be turned off when connected at 100/10Mbps]
7	SFP+ Port 1	SFP+ 1	The first Intel X710-BM2 10/1GbE SFP+ port.  [Top LED will illuminate Orange at 10GbE, LED will be off when at 1GbE]
8	SFP+ Port 2	SFP+ 2	The second Intel X710-BM2 10/1GbE SFP+ port.  [Top LED will illuminate Orange at 10GbE, LED will be off when at 1GbE]
9	Reset Button (recessed)		A momentary switch connected to internal jumpers on the motherboard (see label RSTSW1). Depending on the jumper configuration, this button may perform as either an ACPI Reset or a GPIO button that can be programmed in an OS.  For GPIO mode, the implementation is undefined, and may be polled using I2C or ISA registry examination. In Linux, the ISA address 0x0A00 will return 42 when the button is pressed in GPIO mode, 46 when not pressed in GPIO mode. The register will always read 46 if the device is in ACPI Reset mode, as the button's operation is now undefined for GPIO purposes.

## Rear Panel Configuration



Item #	Object	Label	Description
1, 9, 13, 16	Antenna Ports		Four antenna ports for adding radio antennas (WiFi, LTE, etc.). The ports are covered by plugs while not in use.
2	HDD Activity LED		This amber LED will light up when data activity is detected on an NVMe interface.
3	Power Indicator LED		This LED will stay solid green when the device is powered on.
4	Power Button		Pressing the power Button will power the unit on and illuminate with a blue LED.  <i>In OSes configured to handle ACPI signals, pressing the power button initiates a shutdown.</i>  <i>Pressing and holding the Power Button for 5 seconds will force the unit to power off.</i>
5	DisplayPort Connector		Video and audio output via DisplayPort.

6	SIM Slot		Nano SIM slot for providing a SIM card to an optional internal cellular modem.
7	HDMI Connector		Video and audio output via HDMI.
8	Serial Console Port	COM0	<p>RS-232 serial communications via RJ-45. Default port settings:</p> <ul style="list-style-type: none"> <li>• 115200 baud</li> <li>• No parity</li> <li>• 8 databits</li> <li>• 1 stopbit</li> </ul> <p>AMI firmware will prioritize this over the USB-C Console Port</p>
10	Serial Console Port	COM1	<p>RS-232 serial communications via FTDI FT230XS UART, exposed through USB 3.2 Gen 2 Type C connector. Default port settings:</p> <ul style="list-style-type: none"> <li>• 115200 baud</li> <li>• No parity</li> <li>• 8 databits</li> <li>• 1 stopbit</li> </ul> <p>AMI firmware will prioritize the RJ45 Console Port. To change this, follow the instructions here:  <a href="https://kb.protectli.com/kb/com-port-tutorial/#articleTOC_11">https://kb.protectli.com/kb/com-port-tutorial/#articleTOC_11</a></p>
11	USB2 Connectors		2x USB 2.0 Type-A connectors.
12	USB-C Connector		USB 3.2 Gen 2 <sup>†</sup> Type-C connector with DisplayPort
14	USB2 Connector		USB 2.0 Type-A connector.
15	USB3 Connector		USB 3.2 Gen 2 <sup>†</sup> Type-A connector.

<sup>†</sup>USB-IF naming standard for USB transfer rates: “USB 3.2 Gen 2” is the equivalent and current name for “USB 3.1 Gen 2” offering a theoretical maximum speed of 10 Gigabits (~1.2GB) per second. Older kernels and operating systems may not report the most recent naming convention. For a full linguistic deep dive, please see 3.1 and 3.2 Specification Language Usage Guidelines from USB-IF.

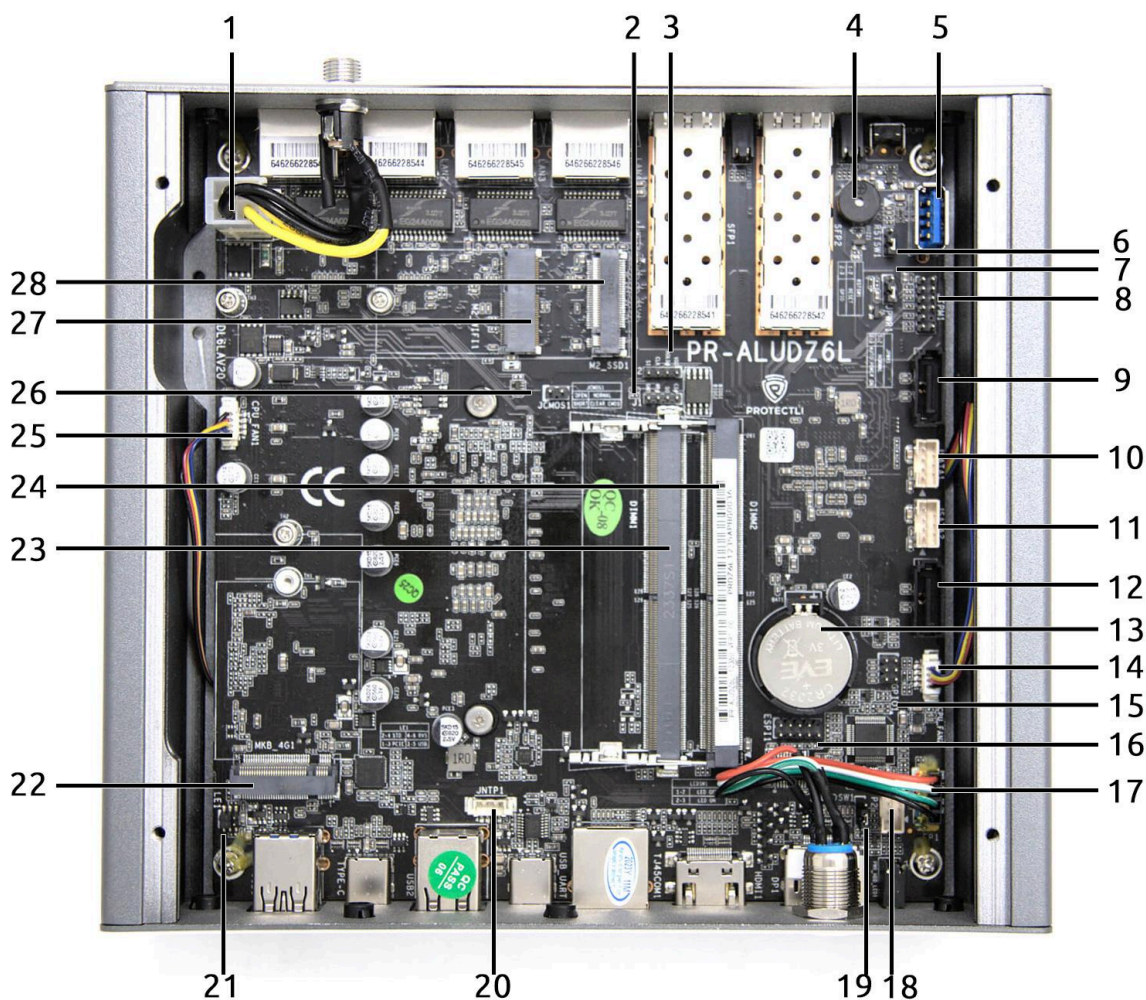
[https://www.usb.org/sites/default/files/usb\\_3\\_2\\_language\\_product\\_and\\_packaging\\_guidelines\\_final.pdf](https://www.usb.org/sites/default/files/usb_3_2_language_product_and_packaging_guidelines_final.pdf)

[https://www.usb.org/sites/default/files/usb\\_3\\_1\\_language\\_product\\_and\\_packaging\\_guidelines\\_final\\_0.pdf](https://www.usb.org/sites/default/files/usb_3_1_language_product_and_packaging_guidelines_final_0.pdf)



# Internal Interfaces

## Motherboard Layout and Pin Configuration



Item #	Object	Label	Description
1	DC IN	DC_IN1	2x2 Molex for +12VDC power.
2	BIOS Programming Headers	J1	One half of BIOS chip jumpers for external programming. 1. VOD 2. HOLD# 3. CLK 4. SI

3	BIOS Programming Headers	J2	One half of BIOS chip jumpers for external programming. 1. CS# 2. SO 3. WP# 4. GND												
4	Buzzer	BUZZ1	PC speaker.												
5	USB3	USB3	Internal USB 3.2 Gen 2 Type-A connector. (Theoretical maximum throughput of 10 Gigabits [~1.2GB] per second)												
6	Reset Button Function Jumper	RSTSW1	Jumper setting determines the functionality of the Reset Button (Front Features, #9) as well as the associated pins on FP1 (Motherboard Top View, #17). <ul style="list-style-type: none"><li>Tied Pins 1-2: ACPI Reset</li><li>Tied Pins 2-3: GPIO (Default)</li></ul>												
7	Power Restore Jumper	JPWR1	Jumper setting determines system state after power is restored after experiencing power loss. <ul style="list-style-type: none"><li>Tied Pins 1-2: Remain powered off</li><li>Tied Pins 2-3: Automatic power on (Default)</li></ul> <p>In order for this behavior to work, you must also change a setting in the AMI firmware menu. Navigate to Advanced&gt;ACPI Config and set Restore ON AC Power Loss to “Pch Control”.</p> <p>The jumper location will not affect the automatic power on function when coreboot 0.9.0 firmware is utilized. It will always be enabled.</p>												
8	TPM	JTPM1	Trusted Platform Module header for a TPM2.0 hardware device. (2x6, 2.0mm pitch) <table><tr><td>Pin 1: VDD</td><td>Pin 2: TPM_CS#</td></tr><tr><td>Pin 3: SPI_MISO</td><td>Pin 4: SPI_MOSI</td></tr><tr><td>Pin 5: NC1</td><td>Pin 6: SPI_CLK</td></tr><tr><td>Pin 7: GND</td><td>Pin 8: SPI_REST</td></tr><tr><td>Pin 9: NC2</td><td>X</td></tr><tr><td>Pin 11: NC3</td><td>Pin 12: TPM_PIRQ#</td></tr></table>	Pin 1: VDD	Pin 2: TPM_CS#	Pin 3: SPI_MISO	Pin 4: SPI_MOSI	Pin 5: NC1	Pin 6: SPI_CLK	Pin 7: GND	Pin 8: SPI_REST	Pin 9: NC2	X	Pin 11: NC3	Pin 12: TPM_PIRQ#
Pin 1: VDD	Pin 2: TPM_CS#														
Pin 3: SPI_MISO	Pin 4: SPI_MOSI														
Pin 5: NC1	Pin 6: SPI_CLK														
Pin 7: GND	Pin 8: SPI_REST														
Pin 9: NC2	X														
Pin 11: NC3	Pin 12: TPM_PIRQ#														
9	SATA Data Connector	SATA1	SATA III data connector. Recommended for additional storage, such as a 2.5” SATA SSD. (Standard 7-PIN SATA III Plug)												

10	SATA Power Connector	JSATA1	SATA power connector for additional storage. (1x4, 2.0mm pitch, JST PH style connector)	
11	SATA Data Connector	JSATA2	SATA power connector for additional storage. (1x4, 2.0mm pitch, JST PH style connector)	
12	SATA Power Connector	SATA2	SATA III data connector. Recommended for additional storage, such as a 2.5" SATA SSD. (Standard 7-PIN SATA III Plug)	
13	CMOS Battery	BAT1	3V CR2032.	
14	CPU Fan Header	CPU_FAN2	Four-pin PicoBlade-compatible header (1.25mm pitch) for included PWM CPU fan located on chassis. The connected fan is 60x60x10MM.	
15	GPIO	GPIO1	General Purpose I/O header. (2x3, 2.0mm pitch) The column containing Pin 1 is the one closest to the CMOS battery.	
			Pin 1: +5V	Pin 2: GND
			Pin 3: GPIO 56	Pin 4: GPIO 57
			Pin 5: GPIO 60	Pin 6: GPIO 61
			AMI firmware allows for the ability to change the 4 GPIO pins between "low", "high", and "input" voltage settings. Low setting registers at 0.0014V and high setting registers at 5.10V. (These settings are found at Advanced>IT8659 Super IO Config>GPIO Config)	
16	ESPI	ESPI1	eSPI header for BIOS chip flashing.	

17	Front Panel Header	FP1	<p>Internal header for adding external device controls and indicators featured through the front panel, such as power button, reset button, activity LEDs, etc. (2x5, 2.54mm pitch) The included power button will be connected to pins 2, 4, 6, and 8. The pinout chart below has been colored to match the baseboard.</p> <table><tr><td>Pin 1: HDD_LED+ [+3.3V]</td><td>Pin 2: PWR_LED+ [+5V]</td></tr><tr><td>Pin 3: :SSD_LED-</td><td>Pin 4: PWR_LED-</td></tr><tr><td>Pin 5: RST_GND</td><td>Pin 6: PW_ON</td></tr><tr><td>Pin 7: RST</td><td>Pin 8: PWON_GND</td></tr><tr><td>Pin 9: No connection</td><td>X</td></tr></table>	Pin 1: HDD_LED+ [+3.3V]	Pin 2: PWR_LED+ [+5V]	Pin 3: :SSD_LED-	Pin 4: PWR_LED-	Pin 5: RST_GND	Pin 6: PW_ON	Pin 7: RST	Pin 8: PWON_GND	Pin 9: No connection	X
Pin 1: HDD_LED+ [+3.3V]	Pin 2: PWR_LED+ [+5V]												
Pin 3: :SSD_LED-	Pin 4: PWR_LED-												
Pin 5: RST_GND	Pin 6: PW_ON												
Pin 7: RST	Pin 8: PWON_GND												
Pin 9: No connection	X												
18	Front Panel Header	FP2	<p>Additional headers for “Front-Panel-like” control. These pins could be used by an auxiliary device to power on, power off, and monitor unit power state.</p> <p>Pin 1 is located closest to the CMOS battery (#13).</p> <table><tr><td>Pin 1: LED +</td><td>Pin 2: LED -</td><td>Pin 3: Power -</td><td>Pin 4: Power +</td></tr></table> <p>Monitoring pins 1 and 2 will dictate if the unit is powered on. Measurement between 1VDC and 3.5VDC indicated the unit is on (S0), 0VDC is powered off (S5).</p> <p>Shorting pins 3 and 4 will emulate an ACPI power button.</p> <ul style="list-style-type: none"><li>Shorting the connection for any duration will send an ACPI command to either power on (S0) if in a powered-off state, or as an ACPI_SHUTDOWN event to be handled by the OS.</li><li>Shorting the connection for over 5 seconds will force the system to enter a soft-off state (S5).</li></ul> <p>The included “SSD Cable Kit” for mounting an additional 2.5” SSD contains a power cable that can be repurposed into a breakout connector as the FP2 and JSATA1/JSATA2 (#10 and #12 respectively) are the same connector size.</p>	Pin 1: LED +	Pin 2: LED -	Pin 3: Power -	Pin 4: Power +						
Pin 1: LED +	Pin 2: LED -	Pin 3: Power -	Pin 4: Power +										

19	LED Control Jumper	LED SW1	<p>Jumper setting determines the operation of chassis LEDs. This will only affect the LED behavior of devices connected to FP2.</p> <ul style="list-style-type: none"><li>• Tied Pins 1-2: LEDs Off</li><li>• Tied Pins 2-3: LEDs On (Default)</li></ul>						
20	External Time Header	JNTP1	<p>Header for use with an external time device, such as a GPS receiver. Serial data is processed by the TPS65994AD Dual Port USB Type-C® and USB PD Controller by way of a slave I²C interface. Pins 1 and 4 are labeled on the motherboard. (1x4, 1.25mm pitch, Molex PicoBlade-compatible)</p> <ol style="list-style-type: none"><li>1. Serial data</li><li>2. Serial clock</li><li>3. +5 VDC</li><li>4. GND</li></ol>						
21	Lane Configuration	LE1	<p>Jumper setting determines the operation mode of MKB_4G1 (#22). Some modems use Pins 20 and 22 for adding additional control functions, or are utilized as voltage sensing pins. Some modems will not work unless proper voltage is applied to specific pins. It is important to reference the documentation of your modem to verify if you need to utilize this switch.</p> <p>One jumper is used to configure voltage on Pin 20:</p> <ul style="list-style-type: none"><li>• Jumped Pins 1-3: [Default] No Voltage on Pin 20</li><li>• Jumped Pins 3-5: 1.83V on Pin 20</li></ul> <p>A second jumper is used to configure Voltage on Pin 22:</p> <ul style="list-style-type: none"><li>• Jumped Pins 2-4: [Default] No voltage on Pin 22</li><li>• Jumped Pins 4-6: 3.3V on Pin 22</li></ul> <p>The following table maps the pins in the same orientation of the photo above. Pin 1 is indicated by a white arrow printed on the motherboard. <b>Please note that even though the default setting is “PCIe signal” according to the silkscreen, this does not add PCIe functionality for components like NVMeS, SSDs, etc.</b></p> <table><tr><td>Pin 2</td><td>Pin 4</td><td>Pin 6</td></tr><tr><td>Pin 1</td><td>Pin 3</td><td>Pin 5</td></tr></table> <p>^</p>	Pin 2	Pin 4	Pin 6	Pin 1	Pin 3	Pin 5
Pin 2	Pin 4	Pin 6							
Pin 1	Pin 3	Pin 5							

22	LTE Expansion Slot	MKB_4G1	Connector uses USB 3.2 signal via an M.2 3052 B-Key, and behavior is affected by the jumpers on the LE1 header (#21). Designed for Protectli cellular modems, but is not limited in its capabilities.
23	Memory Slot	DIMM1	DDR5 SODIMM.
24	Memory Slot	DIMM2	DDR5 SODIMM.
25	CPU Fan Header	CPU_FAN1	Four-pin PicoBlade-compatible header for included PWM CPU fan located on chassis.
26	NVRAM Reset Jumper	JCMOS1	Shorting this jumper while the CMOS battery is connected will reset the BIOS NVRAM.
27	WiFi Expansion Slot	M2_WIFI1	Connector uses PCIe 3.0 x1 protocol over an M.2 E-Key socket. Designed for Protectli WiFi modules, but is not limited in its capabilities.
28	M.2 NVMe Connector	M2_SSD1	Connector uses PCIe 4.0 x4 protocol over an M.2 M-Key socket. It is designed for an NVMe storage device, but is otherwise a functional PCIe port.

## Dimensions View



# Document History

2025-12-15

- Changed wording on LE1 header and clarified behavior of jumpers (#21), and clarified some information regarding the signal used for modem slot (#22)

2025-01-13

- Removed duplicate data from motherboard view table

2025-01-03

- Added "Overview" section
- Added "Included Accessories" section
- Updated section headers for clarity
- Added info about LED behavior for NICs
- Added audio output to DisplayPort capabilities
- Added info about Console redirection prioritizing the RJ45 port
- Changed CPU Fan Header to 2x instead of 1x, added pitch size
- Added pitch size of Trusted Platform Module header
- Removed mention of non-existing USB 2.0 Header and changed to USB 3.2 Gen 2 Type A Port
- Added pitch size for Front Panel Header
- Added USB naming convention notes under "rear features"
- Added pin layout for GPIO header, pitch size, and included information regarding firmware settings with AMI
- Added pin layout for TPM header, pitch size
- Added note to Power Restore Jumper regarding behavior with coreboot and how to configure it with AMI
- Added size of fan in the CPU fan header section as well as pitch of header
- Added FP1 layout, pitch size, and note regarding the fact the power button is connected to this header by default
- Added pitch size and connector type for SATA1 & 2 header
- Added plug type for JSATA1 and 2
- Corrected JSATA2 and SATA2 being improperly labeled as they were swapped
- Added note to LEDSW1 regarding the behavior only affects FP2 and removed mention of it affecting the built in LEDs
- Removed mention of "Designed for Protectli WiFi" on LTE Expansion Slot, reworded to only include Protectli Cellular Modems
- Added note for theoretical speeds for USB3

2024-08-28

- Corrected PCIe specification

2024-08-01

- Changed "PC Speaker" to "PC speaker"
- Changed "RS232" to "RS-232"
- Updated linked spec sheet with ® and ™ as necessary for Intel and AMI
- Changed linked spec sheet from "i226V" to "i226-V"
- Updated linked spec sheet from "4FF SIM" to "Nano (4FF) SIM"

2024-06-28

- Clarified PCI and USB specifications such as speed, protocol, etc.

2024-05-16

- Added additional details regarding the serial port FTDI driver.
- Added details about the Intel X710 SFP+ chipset.
- Added details about how RSTSW1 operation modes affect Front Panel item #9: "Reset Button (recessed)"
- Clarified LTE and/or WiFi slot naming schemes
- Clarified threading on barrel connector
- Updated FP2 (#18 Motherboard View) connector properties and pinout information

2024-04-29

- Initial document